



# DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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## FACULTY PROFILE

Name of the Faculty	M.Sirisha
Designation	Assistant Professor
Email & Phone No	<a href="mailto:Moningi.sirisha@gmail.com">Moningi.sirisha@gmail.com</a> & 7207401570
Date of Joining	04.06.2012
Nature of Association	Regular
Department	Electrical & Electronics of Engineering
Educational Background	1. M.Tech From SRI SIVANI Institute of Technology. 2. B.Tech from SRI SIVANI College of Engineering 3. Intermediate from Sri Chaitanya junior College
Research Areas	Nil
Experience	11 Years



Sl. No	Institute	Designation	Period
1	Dhanekula Engineering College	Asst.Prof	2012 - till date
2	Sri Sivani Group of Institutions	Asst.Prof	2010 - 2012

### List of Publications(National and International Journals)

1. Sirisha M. " Voltage sag compensation using fuzzy control strategy fed DVR for DG Application" , International journal of Engineering & Technology, IJEEE , 2319-8885.

### National/International Conferences ((National and International)

NIL

### Achievements /Awardsetc.,

1. Received Best Mentor Award for the academic Year 2016-17,2018-19

Sirisha.M