



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE



Name of the Faculty	Dr. I SAI RAM
Designation	Professor
Email & Phone No	Inkollusiararam @gmail.com& 9290535464
Date of Joining	21.06.2010
Nature of Association	Regular
Department	Electrical and Electronics Engineering
Educational Background	1. Ph.D From JNTU, Kakinada 2. M.Tech From JNTU, Hyderabad 3. M.S From BITS, Pilani, 4. B.Tech From JNTU, Hyderabad
Research Areas	Power systems
Experience	22Years

S.No	Institute	Designation	Period
1	Dhanekula Institute Of Engineering & Technology, Ganguru	Professor & HOD	21-06-2010 to Till date
2	Guntur Engineering College, Guntur	Professor & HOD	07-06-2009 to 12-04-2010
3	P.V.P.Siddharatha Institue of Technology, Vijayawada	Associate Professor	15-11-2005 to 31-05-2009
4	Koneru Lakshamiah College of Engineering, Vijayawada	Sr Assistant Professor	07-12-2004 to 10-11-2005
5	Sri Vasavi Engg.College, Tadepalligudem	Assistant Professor	01-12-2003 to 1-12-2004
6	Sri Aditya Engg.College, Surampalem	Assistant Professor	01-01-2003 to 31-11- 2003
7	V.R.Siddartha Engg.College, Vijayawada	Lecturer	01-12- 1998 to 31-11-2002

List of Publications (National and International Journals)

1. “Optimal Time-Shift of Renewable Energy Storage Systems in Smart Grid Environment”,
Journal of Control & Instrumentation, Volume 9 , issue2, 2018, Pages 35–45(Scopus Indexed)
2. Optical setting of FACTS devices for voltage stability improvement using PSO adaptive GSA hybrid algorithm "International journal of Engineering science and Technology, an International journal, Volume-19(2016), 1166-1176.(Elsevier),
3. Optical setting of multiple FACTS devices to improve voltage stability, a novel GSA"International journal of Circuits, systems and computers, Volume25,no:6, 2016, DOI:10.1142/S0218126616500584.(SCI)
4. A multi-Objective Hybrid Heuristic approach for Optimal setting of FACTS devices in Deregulated Power System "international journal of IOSR journal of electrical and electronics engineering, ISSN:2320-3331, Volume 11, Issue 4 Ver.I(Jul.-Aug.(2016), PP 90-97.(Impact Factor: 1.419)
5. Comparison of FACTS devices performances used to improve voltage stability based on GA-GSA hybrid algorithm "International journal of hybrid information technology, Volume 2016.(Scopus Indexed)
6. A two stage hybrid optimization approach to improve transmission system performance using FACTS controllers under open access Environment "international journal of Grid & Distribution computing, Volume (2016).(Scopus Indexed)
7. “Overview of Shunt Active Filter used for Reduction of Harmonics in 3-Phase,3-Wire System”, International Journal of Engineering Research(IJER), Volume No.5, Issue No.6(June 2016), pp : 474-478. (Impact Factor: 1.632)
- 8.“Hybrid active power filter using fuzzy dividing frequency control method” International journal of advances in Engineering & Technology, Volume-2, Issue-1, pp 426-432. (Impact Factor: 0.675)
- 9.“Genetically optimized facts controllers for available transfer capability enhancement” International journal of computer applications, Volume 19-No-4, pp 23-27, April 2011.
10. “Sag/swell migration using multi converter unified power quality conditioner” International journal of Advances in Engineering & Technology. Nov 2011, ISSN:2231-1963(Impact Factor: 0.675)

International Conferences ((IEEE))

1. “A Novel hybrid algorithm to enhance voltage stability using UPFC” IEEE International Conference at NIRMA UNIVERSITY, Ahmedabad.
2. “Improvement of Voltage stability by IPFC using GA-GSA Algorithm” IEEE International Conference at NIRMA UNIVERSITY, Ahmedabad.

National Conferences ((IEEE))

1. “Various approaches for optimal Location of FACTS Devices in Deregulated Power Systems - A Critical survey,” “Recent Advancements in Electrical Engineering (RAEE’16)”, Faculty of Engineering, Christ University, Bangalore, September, 2016.

Project Guidance:

No. of projects guided at B.Tech. Level: 20

No. of projects guided at M.Tech. Level: 02

Memberships in Professional bodies/ Academics / societies

- [1]Life Member in Indian Society for Technical education (LMISTEE)
with membership No : LM69060
- [2]Member in Institute of Engineers (M.I.E) with membership No :
M-141255-2
- [3]Senior Member in Indian society of Electrical & Electronics
Engineers (SMISEEE) with membership No :
MISEEE201701701031014

FDPS/Workshops Attended:

S . NO	Type of Event	Total attended
1	FACULTY DEVELOPMENT PROGRAMS	6
2	WORKSHOPS	4
3	WEBINARS	6
4	COURSERA COURSES	3

Achievements / Awards:

1. Received Jai Upadya puraskar for the Academic Year 2017-18
2. Received Jury member appreciation for the Academic Year 2017

Dr SAI RAM INKOLLU