

**Dhanekula Institute of Engineering & Technology**  
**Department of Electronics & Communication Engineering**



DIET/7.5.1/FT 12

**UG TIMETABLE**

Name of the Program: B. Tech in ECE

Class/Sem: III B. Tech SEM-I

Section-B

A.Y: 2020-21

W.E. F: 17/08/2020

DAY	1 9:00-10:00	2 10:00-11:00	3 11:00-12:00	LUNCH	4 02:00-03:00	5 03:00-04:00
MON	DC	AWP	CAO		LICA	DICA
TUE	AWP	CAO	LICA		DICA	DC
WED	PEHV	LICA	DICA		DC	AWP
THU	LICA	DICA	DC		AWP	CAO
FRI	PEHV	DC	DICA		CAO	LICA
SAT	DICA	CAO	AWP		DC	PEHV

Theory	Faculty Name	Total Hours
Computer Architecture & Organization	Mrs.Y.Naga Prasanthi	05
Linear IC Applications	Mr.P.Veerawamy	05
Digital IC Applications	Mr.K.Rama Koteswara Rao	06
Digital Communications	Mrs.K.Radha	06
Antennas and Wave Propagation	Mr.M.Venkateswara Rao	05
Professional Ethics & Human Values (PEHV)	Mr.P.Krishna Reddy	03
<b>Class In charge</b>	<b>Mrs.Y.Naga Prasanthi</b>	

*[Signature]*  
I/C Timetables/Date

*[Signature]*  
HOD/Date  
 HEAD OF THE DEPARTMENT  
 Electronics & Communication Engineering  
 DHANEKULA INSTITUTE  
 OF ENGINEERING AND TECHNOLOGY  
 Ganguru, VIJAYAWADA-521 139

*[Signature]*  
Principal  
 DHANEKULA INSTITUTE  
 OF ENGINEERING AND TECHNOLOGY  
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