



UG TIMETABLE

Name of the Program: B. Tech in ECE

Class/Sem: II B. Tech SEM-I

Section-A

A.Y: 2020-21

W.E. F: 17/08/2020

DAY	1 9:00-10:00	2 10:00-11:00	3 11:00-12:00	LUNCH	4 02:00-03:00	5 03:00-04:00
MON	MEFA	OOPS-JAVA	RVSP		SS	COI
TUE	STLD	RVSP	MEFA		OOPS-JAVA	EDC
WED	COI	EDC	SS		RVSP	MEFA
THU	OOPS-JAVA	STLD	EDC		MEFA	SS
FRI	SS	EDC	STLD		COI	RVSP
SAT	RVSP	SS	STLD		EDC	OOPS-JAVA

Theory	Faculty Name	Total Hours
Electronic Devices and Circuits(EDC)	Mrs.R.Mahalaxmi	05
Switching Theory and Logic Design(STLD)	Mr.Y.Rama Krishna	04
Signals and Systems(S & S)	Mr.A.Sivannarayana	05
Random Variables and Stochastic Process(RVSP)	Mr.P.Krishna Reddy	05
Object Oriented Design & Programming Using Java(OOPS-JAVA)	Dr.S.Suresh	04
Managerial Economics & Financial Analysis(MEFA)	Mr.Y.Priya Sagar	04
Constitution of India(COI)	Mrs.M.Silpa	03
Class In charge	Mrs.R.Mahalaxmi	

Pd
I/C Timetables/Date
13/8/2020

HOD
HEAD OF THE DEPARTMENT
Electronics & Communication Engineering
DHANEKULA INSTITUTE
OF ENGINEERING AND TECHNOLOGY
Ganguru, VIJAYAWADA-521 139

13/8/2020
DHANEKULA
OF ENGINEERING AND TECHNOLOGY
Ganguru, Vijayawada-521 139

