

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
Department of Electronics & Communication Engineering



DIET/7.5.1/FT 12A

UG TIME TABLE

Name of the Program: B.Tech in ECE

Class/Sem: III B.Tech SEM-II

Sec: B

Class Room No: F-40

A.Y: 2019-20

W.E.F: 02/12/2019

DAY	1 9:00-9:50	2 9:50-10:40	3 10:50-11:30	4 11:30-12:20	5 12:20-1:10	LUNCH	6 2:00-2:50	7 2:50-3:40	8 3:40-4:30
MON	MWE	BME	MPMC/DC LAB				VLSI	BME(T)	SPORTS
TUE	BME	MPMC	MWE(T)	DSP	COUNS		CRT		IPR
WED	DSP	VLSI	MWE	MPMC(T)	VLSI		ITC		
THU	MPMC	MWE	BME	DSP	DSP(T)		VLSI/MPMC LAB		
FRI	VLSI	MPMC	DC/VLSI LAB				ASSOCIATION ACT.		
SAT	MPMC	IPR	INET	DSP	MWE		VLSI(T)	BME	LIBRARY

Theory/Lab	Faculty Name	L	T	P	O	Sign
Micro Processors & Micro Controllers	Mr.A.Sivannarayana	4	1	--	--	<i>Amz</i>
Micro Wave Engineering	Mr.P.Rama Krishna	4	1	--	--	<i>P.R.K</i>
VLSI Design	Dr.G.L.Madhumati	4	1	--	--	<i>G.L.M</i>
Digital Signal Processing	Dr.K.Durga Rao	4	1	--	--	<i>K.D.R</i>
Bio-Medical Engineering	Dr.P.Pavithra Roy	4	1	--	--	<i>P.P.R</i>
IPR & Patents	Mr.N.Naga Raju	1	1	--	--	<i>N.N.R</i>
Micro Processors & Micro Controllers Lab	Mr.A.Sivannarayana	--	--	3	--	<i>Amz</i>
VLSI Lab	Mrs.I.Pavani Prapurna	--	--	3	--	<i>I.P.P</i>
Digital Communications Lab	Mrs.V.B.S.S.Nagini	--	--	3	--	<i>V.B.S.S.N</i>
Association	Mrs.I.Pavani Prapurna Mr.Y.Rama Krishna	--	--	--	3	<i>I.P.P</i>
ITC	Mrs.M.Mythri/Mr.S.Chandrasekhar	--	--	--	3	<i>M.M</i>
Internet	Mr.A.Sivannarayana	--	--	--	1	<i>Amz</i>
CRT	Mr.V.Ramana Rao/Ms.G.Visali	--	--	--	3	<i>V.R.R</i>
Library	Mr.A.Sivannarayana	--	--	--	1	<i>Amz</i>
Counseling	Mrs.RML/MrS.IPP/Dr.PPR/Mr.NNR	--	--	--	1	<i>R.M.L</i>
Sports	Mr.P.Rama Krishna	--	--	--	1	<i>P.R.K</i>
Class Incharge	Mr.A.Sivannarayana					

I/C Time Tables/Date

HOD / Date

Principal



Principal
DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY
 Ganguru, Vijayawada-521 139