

**DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY**  
**Department of Electronics & Communication Engineering**



DIET/7.5.1/FT 12A

**UG TIME TABLE**

Name of the Program: B.Tech in ECE

Class/Sem: II B.Tech SEM-II

Sec: A

Class Room No: F-36

A.Y: 2019-20

W.E.F: 02/12/2019

DAY	1 9:00-9:50	2 9:50-10:40	3 10:50-11:30	4 11:30-12:20	5 12:20-1:10		6 2:00-2:50	7 2:50-3:40	8 3:40-4:30
MON	ECA	CS	AC	INET	CS	<b>LUNCH</b>	ECA/AC LAB		
TUE	EMWTL	AC	MS	ECA	PDC		CS(T)	CRT	SPORTS
WED	EMWTL	PDC	AC/ECA LAB				EMWTL	AC	PDC(T)
THU	AC	MS	PDC	COUNS	ECA(T)		ITC		
FRI	PDC	CS	EMWTL	ECA	EMWTL(T)		CS	ECA	MS(T)
SAT	MS	ECA	MS	EMWTL	LIBRARY		ASSOCIATION ACT		AC(T)

Theory/Lab	Faculty Name	L	T	P	O	Sign
Electronic Circuit Analysis	Mrs.R.Mahalaxmi	5	1	-	-	<i>[Signature]</i>
Control Systems	Mr.P.T.Krishna Sai	4	1	-	-	<i>[Signature]</i>
Electromagnetic Waves and Transmission Lines	Mr.K.Venkata Seshagiri Rao	5	1	-	-	<i>[Signature]</i>
Analog Communications	Mr.K.Ramakoteswara Rao	4	1	-	-	<i>[Signature]</i>
Pulse and Digital Circuits	Mr.P.Veerawamy	4	1	-	-	<i>[Signature]</i>
Management Science	Mr.Y.Priya Sagar	4	1	-	-	<i>[Signature]</i>
Electronic Circuit Analysis Lab	Mrs.R.Mahalaxmi	-	-	3	-	<i>[Signature]</i>
Analog Communications Lab	Mr.K.Ramakoteswara Rao	-	-	3	-	<i>[Signature]</i>
Association	Mrs.I.Pavani Prapurna Mr.Y.Rama Krishna	-	-	-	3	<i>[Signature]</i>
ITC	Mrs.M.Mythri/Mr.S.Chandra Sekhavar	-	-	-	3	<i>[Signature]</i>
CRT	Mr.V.Ramana Rao/Ms.G.Visali	-	-	-	2	<i>[Signature]</i>
Internet	Mr.K.Venkata Seshagiri Rao	-	-	-	1	<i>[Signature]</i>
Library	Mr.P.Veerawamy	-	-	-	1	<i>[Signature]</i>
Counseling	Mr.ASN/Mr.PKR/Mr.YRK/Mr.KVSR	-	-	-	1	<i>[Signature]</i>
Sports	Mr.K.Ramakoteswara Rao	-	-	-	1	<i>[Signature]</i>
Class Incharge	Mr.K.Ramakoteswara Rao					<i>[Signature]</i>

I/C Time Tables/Date

HOD / Date

Principal



Principal  
**DHANEKULA INSTITUTE**  
 OF ENGINEERING AND TECHNOLOGY  
 Ganguru, Vijayawada-521 139