



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Faculty Publications for Academic year: 2015-2016

S.No	Faculty Name	Paper / Book title	Events / Journals / Publisher
1	Mrs. K. Lakshmi Sowjanya	Low Power and Area Efficient Fused Floating Point FFT Architecture	International journal of scientific engineering and technology, ISSN 2319-8885 Vol.04, Issue.25, July-2015,
2	Mr. K. Rama Koteswara Rao	Implementation of Single Precision Floating Point Multiplier	International Journal & Magazine of Engineering Technology, management and Research (IJMETMR), Vol.2, Special issue:9, pp. 2348-4845, September 2015.
3	Dr.G.L.Madhumati	Implementation and Verification of I ² C Single Master Multiple Slave Bus Controller using SystemVerilog and UVM	International Journal of VLSI System Design and Communication Systems, Management and Research (IJVDCS), Vol.03, Special issue.08, pages. 1239-1243, October-2015.
4	Mr.S.Chandra Sekhar	128 Bit Parallel Prefix Tree Structure Comparator	International Journal of Research in Engineering and Science (IJRES), Volume 3 Issue 10, October. 2015.
5	Mr.A.Sivannarayana	FFT Design Using Reliable Multiplier with Adaptive Hold Logic	International journal and magazine of engineering and technology, management and research, ISSN:2348-4845 PP: 1178-1185, Volume 2, Issue 10, Oct 2015.
6	Mr. K.Suresh Babu	Low Power Efficient MAC Unit using Proposed Carry Select Adder	International journal and magazine of engineering and technology , management and research, ISSN:2348-4845 PP: 1186-1191, Volume 2, Issue 10, Oct 2015
7	Mr.S.Chandra Sekhar	Direct Design of Reversible Combinational and Sequential Circuits Using PSDRM Expressions	International Journal of Research in Engineering and Science (IJRES), Volume 3 Issue 11, November. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.
8	Mr.M. Tulasidas	Steganography based secret image sharing using block division technique	Presented a paper at computational intelligent and communication networks(CICN-2015), Jabalpur on 12 th to
9	Dr.G.L.Madhumati	Wireless Secured Data Transmission using Cryptographic Techniques through FPGA	International Journal of Engineering and Technology (IJET), e-ISSN : 0975-4024, p-ISSN :2319-8613 Vol 8 No 1 Feb-Mar 2016,pp. 332-338

10	Mr. P. Krishna Reddy	Reduction of Static Power by using Biasing and Body Biasing Techniques	International Journal of Scientific Engineering and Technology Research Volume.05, IssueNo.06, March- 2016, Pages: 1008-1014,ISSN 2319-8885
11	Mr. V.Amareswar	Implementation of WG Stream Cipher using Encryption and Decryption Algorithm	Advances in Electrical and Computer Engineering ISSN: 1582- 7445,pp.169-174
12	Dr.G.L.Madhumati	Zig Bee Based Wireless Data Transmission with LDPC codes using FPGA	International Journal of Engineering and Technology (IJET), e-ISSN: 0975-4024, p-ISSN: 2319-8613 Vol. 8 No 2 April-May 2016 pp. 653-659.
13	Mr.S.Chandra Sekhar	A prototype controlling of barrage gates using Ardino under flood conditions	IET& IEEE 3 rd International conference on Electrical, Electronics, Engineering trends, Communication, optimization and sciences(EEECOS-2016) organized by Sasi Inst. Of Tech. & Engg., Tadepalligudem, West Godavari during 1 st to 2 nd June 2016
14	Mr.S.Chandra Sekhar	Performance analysis of image fusion for different wavelet families	IEEE International conference on innovation in information embedded and communication systems (ICIIECS'16) organized by Karpagam College of Engg., Coimbatore held in March 2016.