



# DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### Faculty Publications for Academic year: 2014-2015

S.No	Faculty Name	Paper / Book title	Events / Journals / Publisher
1	Mr.V.Amareswar	"Design of Error detection and data recovery architecture for motion Estimation"	International Journal of VLSI System Design and communication systems, ISSN 2322-0929 Vol.02, Issue.09, November-2014, Pages:0928-0932
2	Mr.K.Suresh Babu	"Design and implementation of an enhanced and power optimized correlator in OFDM applications "	International Journal of Scientific Engineering and Technology Research, ISSN 2319-8885 , Volume.03, IssueNo.45, December- 2014, Pages: 9246-9248
3	Mr.A.Gowtham Kumar	"A small size CPW FET five slot antenna for UWB application"	International Journal of Engineering & Science Research, ISSN 2277-2685 IJESR/October 2014/ Vol-4/Issue- 10/634-637
4	Mr.P.Krishna Reddy	Establishing low latency ETI encoder for low power application	International Journal of Scientific Engineering and Technology Research, ISSN 2319-8885 Vol.03,Issue.39 November-2014, Pages:7978-7981
5	Mrs.T.Haritha	"Intelligent irrigation control system"	SSRG International Journal of Electronics and Communication Engineering (SSRG-IJECE) – volume1 issue10 Dec 2014, Pages: 25-31
6	Mr.V.Venkatesh	"Robust Document in image binarization technique for degraded document images "	International Journal of Advanced Technology and Innovative Research Volume. 06, IssueNo.08, October-2014, Pages: 765-769
7	Dr. G.L.Madhupati	"FPGA Implementation of SIFT Algorithm Using Xilinx System Generator"	International Journal of Emerging Trends in Electrical and Electronics (IJETEE – ISSN: 2320-9569) Vol. 10, Issue. 10, Oct. 2014, Pages-80-85
8	Mr.P Ramakrishna	An efficient low power current comparison based domino logic	International Journal of Research and development in science and technology Volume 1 Issue 2, may. 2015.pp 26-32, ISSN (print): 2350-4751, ISSN (online): 2350-4751.
9	Mrs.M Mythri	An enhanced logarithmic Number system adder/subtractor for low power DSP applications	International Journal of Research and development in science and technology Volume 1 Issue 2, June. 2015.pp 19-25 ISSN (print): 2350-4751, ISSN (online): 2350-4751.
10	Mr.S.Chandra Sekhar	Implementation and Design of AES S-Box on FPGA	International Journal of Research in Engineering and Science (IJRES), Volume 3 Issue 1, Jan. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.