

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Faculty Publications for Academic year: 2013-2014

S.No	Faculty Name	Paper / Book title	Events / Journals / Publisher
1	Mr. P.Krishna Reddy	Analysis of Elliptical and	IJSETR/Volume 2, Issue 5,May
1	1711. 1 .1xi isimia ixeddy	Circular Patch Antennas in	2013,ISSN:2278-7798,PP 1194
2	Mr. K.Satya kiran	A Novel Adaptive Algorithm	
	,	Applied to Noise Cancellation.	
3	Mr.P.Veera Swamy	Design and Analysis of DS-	HCED Volume 2 Ione 7 ICCN 2250
		CDMA detected multi path	IJCER, Volume 3,Issue 7,ISSN:2250-
		signal using rake receiver	3005, PP.26
_		simulatorforwirelessWirelessSensors& Video	International Journal of Engineering
4	Mrs.Haritha Tummala	Networks (WSVN) For	Research and Applications (IJERA)
		indication of fungus affected	ISSN: 2248-9622 ,Vol. 2, Issue 3, May-
		· ·	
		plants in an Agricultural Field	Jun 2012, pp.1388-1390 International Journal of Electronics
5	Mrs.Haritha Tummala	Design and Implementation of	Communication and Computer
		Communication and Warning	Engineering, Volume 4, Issue 3, ISSN
		Sub-Systems of a Chemical	(Online): 2249–071X, ISSN (Print):
		Detector	2278–4209
6	Mrs.Haritha Tummala	A G G G 1 1 A	International Journal of Engineering
		A Survey On Chemical Agent	Research & Technology (IJERT) Vol. 2
		Detectors	Issue 3, March – 2013 ISSN: 2278-0181
7	Mrs.Haritha Tummala	An Introduction to Wireless USB Flash Devices	International Journal of Engineering
			Trends and Technology (IJETT) -
			Volume4Issue4- April 2013, ISSN:
			2231-5381 Page 924
8.	Mr. K.Satya Kiran	Design and modelling of Modulo Multipliers using RNS	National Conference on Innovations in
			Engineering & Technology (NCIET-
			2013) at Gudlavalleru Engineering
			College, Gudlavalleru, December 2013
9	Dr. G.L.Madhumati	HDL Implementation &	International Journal of Applied
		Performance Comparison of	Engineering Research ISSN 0973-4562
		Various Filtering Techniques	Volume 9, Number 18 (2014) pp. 5085-5094.
		using FPGA	
10	Dr. G.L.Madhumati	Real Time Delay Application for	International Journal of Applied
		Digital Circuits with Peripheral	Engineering Research ISSN 0973-4562
		Based Digital Clock Using	Volume 9, Number 18 (2014)
		FPGA	pp. 5115-5123.
11	Dr. G.L.Madhumati	Edge detection modeling through	International Conference On Devices,
		reconfigurable devices in image	Circuits And Systems – ICDCS 2014,
		processing applications using	Department Of ECE, Karunya
		XPS	University, Coimbatore, India