



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE FILE

Name of the Faculty	VUNDI SUBBA RAJU
Designation	Assistant Professor
Date of Joining	1.6.2012
Nature of Association	Regular
Email & Phone No	vsvnraju@gmail.com & 9666222531
Department	Electronics & Communication Engineering
Educational Background	1. B.Tech From JNTU UK 2. M.TECH From JNTUK
Area of Specialization	VLSI Design
Research Areas	Low power VLSI Design,IOT,Embedded Systems
Experience	11 Years



Sl. No	Institute	Designation	Period
2	Dhanekula Institute of Engineering & Technology	Asst.Prof	2012 - till date
1	Anurag Engineering College	Asst.Prof	2009-2010

List of Publications (National and International Journals)

1. Rameem Sultana , Mr. V. Subba Raju “FPGA Implementation of 32-Bit Partially Parallel Encoder Architecture for Long Polar Codes.” International Journal of Research p-ISSN: 2348-6848 e-ISSN: 2348-795X Volume 03 Issue 18 December2016
2. V. Subba Raju, M. Chaitanya Kiran “An Adaptive randomized Pattern matching algorithm for Assured Detection in high complex data bases.” International Journal of Applied Research & Studies IJARS/Vol.I/ Issue I/Jun-Aug, 2012/125

Seminars / Workshops

1. Attended Two day National level workshop on Recent advances in signal processing held at LBRCE, Mylavaram during 9,10th May 2011.
2. Attended one day workshop ARM based Embedded Systems held at IETE Vijayawada Center on 6th Aug 2012.
3. Attended Five days Training Program on Instructional Design & Delivery held at DIET,Ganguru during 29th Oct to 2nd Nov 2012.

4. Attended Three Days VLSI work shop on Digital Design & Verification using Verilog held at DIET,Ganguru during 12th to 14th Feb 2013.
5. Attended One Day Workshop on GNU/Linux held DIET,Ganguru on Sep20th 2013.
6. Attended Two Day Workshop on Embedded System /design using TI MSP430 Microcontroller Development tools held at PVPSIT, Kanuru during Feb 2nd to 3rd 2015.
7. Attended Two Day Workshop on Introduction to Robotics held at Vignan University, Guntur during Feb 26th to 27th 2015.
8. Attended one day Workshop on Introduction to IOT held at VRSEC, Kanuru organized by TEQIP II on Sep 25th 2016.
9. Attended 32nd National Convention & National Seminar on Intelligent System for More Connected World: IOT Organized by APSSDC during Oct 24th –25th 2016.

FDP`s:

1. Attended Faculty development Program on Outcome Based Education held at DIET, Ganguru Organized by E&ICT, NIT, Warangal during Jun 6th – 11th 2016.
2. Attended Two week FDP on Digital VLSI Circuit Design organized by NIT, Warangal from Jun 3rd -12th 2017.
3. Attended One week workshop on Internet of Things Organized by NIT, Warangal at DIET, Ganguru, 2017.

Other Participations / Achievements:

1. Received felicitation from LION`s Club International for NSS coordinator Works
2. Received Appreciation certificate from D.Gautam Sawang IPS,Vijayawada for offering sincere and dedicated service during Krishna Pushkaralu 2016

V.SUBBA RAJU