



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PRPOFILE

Name of the Faculty	KRISHNA SALADI
Designation	Assistant Professor
Date of Joining	26-10-2020
Nature of Association	Regular
Email & Phone No	15789krishna@gmail.com & 6303252385
Department	Electronics and Communication Engineering
Educational Background	1.Ph.D (Pursuing),JNTU Kakinada 2. M.Tech (VLSI & Embedded Systems) from RVCE,Bangalore 3. B.Tech (ECE) from AIET, Vizag
Areas of Specialization	Digital VLSI Design
Research Areas	Digital VLSI Design, Arithmetic circuits
Experience	08 Years



Sl. No	Institute	Designation	Period
1	DhaneKula Institute of Engineering & Technology, Ganguru	Assistant Professor	26-10-2020 - till date
2	Aditya Engineering College, Surampalem	Assistant Professor	01-06-2016- 23-10-2020
3	Vignan University	Assistant Professor	02-07-2012 - 19-07-2014
4	Lovely Professional University	Assistant professor	21-07-2011-27-04-2012
5	NITW	Adhoc Faculty	13-07-2010-15-12-2011
6	APS College of Engineering	Lecturer	18-08-2009-14-06-2010

List of Publications (National & International Journals):

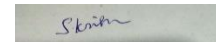
1. Sambaiah, P., Venkatareddy, V., Krishna, S., & Polaiyah, G. (2014). A Tuned Resistive Feedback CMOS Low Noise Amplifier for Multiband Applications. International Journal of Applied Engineering Research, 9(19), 6413-6421.

List of Publications (National & International Conferences):

1. N. V. Satyanarayana, Krishna Saladi, R. Bharath Kumar "Review on Compressors based Approximate Multipliers Design" 5th International Conference on Computing Methodologies and Communication (ICCMC 2021), 08-10, April 2021
2. Krishna saladi and Leela Kumari B, "Adiabatic Logic-Based Area and Energy-Efficient Full Adder Design" International conference on Intelligent Computing in Control and Communication (ICCC 2020), August 7-8, 2020
3. Yamini Devi Ykuntam , Katta Pavani and Krishna Saladi , "Design and analysis of High speed wallace tree multiplier using parallel prefix adders for VLSI circuit designs" Eleventh International Conference on Computing, Communication and Networking Technologies (ICCNT 2020), July 1-3
4. Venkatareddy vemireddy, Sambaiah pelluri and krishna saladi, "Low Power Reactive Feedback CMOS Low Noise Amplifier", 2014 Communication and Signal Processing Conference (ICCSP), Chennai.
5. Krishna saladi and B S Kariyapaa "Design and Implementation of Compression Algorithms Using MIL-1553B bus" at NACTECIT-2009

Achievements / Awards etc.,

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| 1. UGC NET Qualified | : April 2014 |
| 2. GATE Qualified | :2013 |
| 3. GATE Qualified | :2007 |



Krishna Saladi