



# DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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## FACULTY PROFILE

<b>Name of the Faculty</b>	Sivannarayana Amallapudi
<b>Designation</b>	Assistant Professor
<b>Date of Joining</b>	3.9.2013
<b>Nature of Association</b>	Regular
<b>Email &amp; Phone No</b>	asn.488@gmail.com & 9490241040
<b>Department</b>	Electronics and Communication Engineering
<b>Educational Background</b>	1. M. Tech From KLU, Guntur 2. B. Tech from Nalanda Institute of Engg & Tech., JNTUK
<b>Area of Specialization</b>	VLSI Design
<b>Research Areas</b>	VLSI low power Design and VLSI Signal Processing
<b>Experience</b>	8.3 Yrs



Sl. No	Institute	Designation	Period
1	DhaneKula Institute of Engineering & Technology	Asst. Prof	2013 - Till date

### List of Publications (International Journals)

1. A. Sivannarayana "Design and Modeling of Modulo Multipliers using RNS", International Journal of Innovative Technology and Exploring Engineering, ISSN: 2278 – 3075, Volume-3, Issue-2, July 2013.
2. AVV Hanuman Sai Krishna and A. Sivannarayana "FFT Design using Reliable Multiplier with Adoptive Hold Logic", International Journal & Magazine of Engineering, Technology, Management and Research, ISSN NO: 2348 – 4845, Volume-2, Issue NO- 10, October 2015.
3. V. Nandini and A. Sivannarayana, "FIR Filter Implementation using Novel Modulo  $2^n - 2^k - 1$  Adder for RNS", International Journal & Magazine of Engineering, Technology, Management and Research, ISSN NO: 2348 – 4845, Volume No – 3, Issue No- 10, October 2016.

**A. Sivannarayana**