



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE

Name of the Faculty	RamaKrishna Payyavula
Designation	Assistant Professor
Date of joining	3-6-2013
Nature of Association	Regular
Email & Phone No	prk.2418@gmail.com & 9491241800
Department	Electronics & Communication Engineering
Educational Background	M.Tech From Sana Engineering College, Kodad B.Tech From Anurag Engineering College, Kodad
Area of Specialization Experience	Embedded Systems & VLSI Design 10 Years



Sl. No	Institute	Designation	Period
1	DhaneKula Institute of Engineering & Technology	Asst. Prof	2013 - till date
2	Amrita Sai Institute of Science & Technology	Asst. Prof	2011-2013

List Of Publications:

1. **P.RamaKrishna** and Ch.Mohan Sai Kumar “Smart Driving with Drowsiness Detection and Alert system”
A Journal of Composition Theory Volume No: XIII Issue No: I JANUARY 2020 ISSN NO: 0731-6755
2. P.Vani, V.Santhosh Kumar and **P.RamaKrishna** “A Novel Fuzzy Filter for Random Impulse Noise Removal in a Color Video” *International Journal & Magazine of Engineering, Technology, Management and Research.* Volume No: 2 (2015), Issue No: 7 (July) ISSN No: 2348-4845
3. A.Mahesh and **P.RamaKrishna** “An Efficient Low Power Current Comparison Based Domino Logic”
International Journal of Research & Development in Science and Technology. Volume 1, Issue2 , May 2015, PP 26-32 ISSN 2350-4751 (Print) & ISSN 2350-4751(Online)

Short Term Training Programs Attended:

1. A 6-Day AICTE Sponsored STTP on **Mixed Signal Design Approaches for Artificial Intelligence Processors** during 28/12/2020 to 03/01/2021 Organized by Lakireddy Bali Reddy College of Engineering
2. A 6-Day STTP on Fundamentals of **Radar Signal Processing-2** during 12/08/2020 to 17/08/2020 Organized by RISE Krishna Sai Prakasam Group of Institutions in association with IETE

Faculty Development Programs Attended:

1. A 2-Week AICTE approved Faculty Development Program on **Pedagogy for Online and Blended Teaching-Learning Process**, during 30/10/2018 to 13/12/2018 Organized by IIT Bombay
2. A 2-Week AICTE approved Faculty Development Program on **Foundation Program in ICT for Education**, during 13/09/2018 to 18/10/2018 Organized by IIT Bombay
3. A 5-Day Faculty Improvement Program on **VLSI Design** during 15/11/2016 to 19/11/2016 at SRK Institute of Technology, Organized by APSSDC.
4. A 3-Day Faculty Development Program on **Custom IC Design and ASIC Implementation Using Cadence EDA Tools**, during 03/08/2016 to 05/08/2016 Organized by Dhanekula Institute of Engineering & Technology in Technical Collaboration with ENTUPLE Technologies
5. A 2-Day Faculty Development Program on **Analog System Design using ASLK Pro**, during 16/02/2016 to 17/02/2016 Organized by Dhanekula Institute of Engineering & Technology in association with EdGate Technologies Pvt Ltd
6. A 2-Day Faculty Development Program on **Advanced Digital Signal Processing & Applications**, during 12/09/2015 to 13/09/2015 Organized by VR Siddhartha Engineering College Sponsored by SAGTE and TEQIP II

Workshops Attended:

1. A 1-Day Workshop on **GNU/LINUX** organized by Dhanekula Institute of Engineering & Technology

Coursera Certification Courses Attended:

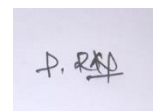
1. Wireless Communications for Everybody
2. Sensors and Sensor Circuit Design
3. AI for Everyone

Technical E-Quiz's Attended:

1. Participated in A National Level Technical E-Quiz on **Signals and Systems** Organized by Yogananda Institute of technology and Science on 14/07/2020 got 95%

Professional Memberships:

1. Have life time membership in **Indian Society for Technical Education**, ISTE
2. Have life time membership in **Indian Society of Systems for Science & Engineering**, ISSE



RamaKrishna Payyavula