# DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
## (Approved by AICTE Affiliated to JNTU Kakinada)
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## FACULTY PRPOFILE

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|---|---|
| **Name of the Faculty** | Mythri Myneni |
| **Designation** | Assistant Professor |
| **Date of Joining** | 3.6.2013 |
| **Nature of Association** | Regular |
| **Email & Phone No** | mythri.ece07@gmail.com & 9010312269 |
| **Department** | Electronics & Communication Engineering |
| **Educational Background** | 1. M.Tech(VLSI Design) From SATHYABAMA UNIVERSITY. <br><br> 2. B.E From IECW,ANNA UNIVERSITY. |
| **Area of Specialization** | VLSI Design |
| **Research Areas** | |
| **Experience** | 10.5 Years |

| Sl. No | Institute | Designation | Period |
|---|---|---|---|
| 3 | Dhanekula Institute of Engineering & Technology | Asst.Prof | 2013 - till date |
| 2 | MIC Engineering College | Asst.Prof | 2011 - 2013 |
| 1 | Mamallan Institute of Engineering& Technology | Lecturer | 2010 - 2011 |

**List of Publications (National and International Journals)**

1. "Testing of Memory Using Franklin Method" paper published in an International Journal IJRET (ISSN:2319-1163) in December 2012 publication.

2. An Enhanced Logarithmic Number System Adder/Subtractor for Low power DSP applications T.R.S. Chandran[1] , M. Mythri[2] , [1] M.Tech (VLSI Design), Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada. [2] M-Tech(VLSI Design), Assistant Professor, Dhanekula Institute of Engineering and Technology,, Ganguru,Vijayawada. International Journal of Research & Development in Science and Technology Volume 1, Issue2 , June 2015, PP 19-25 ISSN 2350-4751 (Print) & ISSN 2350-4751(Online).

3. IMPLEMENTATION OF AN ERROR DETECTION SYSTEM USING CONCURRENT BIST 1 . Mantrala Praneetha, 2 . M.Mytri 1 . PG Scholar, VLSI, Dhanekula Institute of Engineering & Technology, 2 . Assistant Professor, ECE, Dhanekula Institute of Engineering & Technology, Ganguru, Vijayawada in International Journal of Engineering In Advanced Research Science and Technology ISSN:2278-2566, Volume3 Issue-2.

4. "FFT Implementation of Multi-Precision Based Dynamic Voltage Scaling Multiplier with Operands Scheduler" in International Journal & Magazine of Engineering, Technology, Management and Research , ISSN No:2320 - 3706(Print) ISSN No:2348 - 4845(Online)


**National/International Conferences ((National and International)**

1. *"*Testing of RAM using Pattern Sensitivity Method" selected in National conference in Karpagam College Of Engineering on 1$^{st}$ October 2010.

**Achievements / Awards etc.,**

NIL

**Mythri Myneni**