

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE, Affiliated to JNTUK Kakinada)

Programmes Accredited by NBA: B.Tech in CE, MEC, EEE & ECE

NAAC Accredited & An ISO 9001-2015 Certified Institution

Ganguru, Vijayawada – 521 139. Phone: 8333924842/43, 9441675588, Mob: 9491017088

Email: diet.principal@gmail.com, principal@diet.ac.in, Website: www.diet.ac.in

Faculty Profile

Name of faculty Mr. Chandra Sekhar Savalam
Designation Assistant Professor
Department Electronics and Communication Engineering



Date of Joining 01.11.2012

Nature of Association Regular

Email csekhar.savalam@gmail.com, css@diet.ac.in

Phone Number +91 94404 64408

Educational Background Pursuing Ph.D. (VLSI Design), JNTUK University, Kakinada

M.Tech (VLSI SD), JNTUK University, Kakinada

B.Tech (ECE), JNTUH University, Hyderabad

Total Experience 14 Years

Industrial Experience -

Teaching Experience 14 Years

Area of Specialization VLSI Design

Research Areas VLSI Design, Error Correcting Codes, FPGA Design and Signal Processing.

Experience:			
S.No	Institute/Industry	Designation	Period
1.	Dhanekula Institute of Engineering & Technology	Asst.Professor	1.11.2012–till date
2.	MVR College of Engineering and technology, Paritala. Vijayawada.	Asst.Professor	08-07-2010 to 17-10-2012
3.	Raja Mahendra College of Engineering and Technology, Hyderabad.	Asst.Professor	07-11-2007 to 13-10-2009
4.	Anwarul uloom College of Engineering and Technology, Vikarabad.	Asst.Professor	05-06-2006 to 31-10-2007

List of Publications (National and International Journals)

- Korapati Prasanti, S. Kalpana, **Chandra Sekhar Savalam**, "Protection of Cultivated Fields during Heavy Rains and Air" *Journal of Green Engineering*, Volume-10, Issue-12, December 2020, Page No. 13311-13320. (Scopus Indexed)

- 2 **Chandrasekhar Savalam** , Shaheda Sultana, MD.Tarunam Jahan , N.Durga Prasad , P.Komali,
“Design and Implementation of Reverse Carry Propagate Adder(RCPA) on FPGA”
 International Journal of Innovative Engineering and Management Research (IJIEMR), Vol 09
 Issue07, July 2020.
- 3 **S.Chandra Sekhar**, Prasanti Korapati,**“Design and Analysis of Gray to Binary converter
 using Quantum Dot Cellular Automata(QCA)”** Test Engineering and Management, March-
 April 2020 ISSN: 0193-4120 Page No. 13018 – 13023
- 4 **S.Chandra Sekhar**, Prasanti Korapati, AS Haranadh,**“ Design and Implementation of High
 performance and low power Mixed logic line Decoders”** International Journal of Innovative
 Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-8, Issue- 6S4, April
 2019. (Scopus Indexed)
- 5 D.Yamini , **S.Chandra Sekhar**, **“10T SRAM-VDD pre-charge using read port for low switching
 power and low RBL leakage”** International Journal of Scientific Development and Research
 (IJS DR), *Volume 5 Issue 15 May 2018. e-ISSN: 2348-6848, p-I ISSN: 2348-795X.*
- 6 P Divya, **S.Chandra Sekhar**, **“Carry skip adder (CSKA) high-speed operating under wide
 range of supply logic at different levels”** International Journal of Scientific Development and
 Research (IJS DR), *Volume 3 Issue 4 April 2018. ISSN: 2455-2631.*
- 7 CH. Harika, **S.Chandra Shekhar**, **“Systematic Error Correcting Codes Implementation for
 Matching of Data Encoded”** International Journal of Professional Engineering Studies
 (IJPRES), *Volume VIII Issue 2 ,January 2017.*
- 8 T.Sikhamani, **Chandrasekhar Savalam**, **“Design of Fault Tolerant Parallel FFT’s Using Error
 Correction Codes and Parseval Checks”** International Journal & Magazine of Engineering,
 Technology, Management and Research (IJMETMR) ,*Volume 4 Issue 5 May 2017. ISSN No:
 2348-4845.*
- 9 G.Janani , **S.Chandra Sekhar**, **“Direct Design of Reversible Combinational and Sequential
 Circuits Using PSDRM Expressions”** International Journal of Research in Engineering and
 Science (IJRES), Volume 3 Issue 11, November. 2015. ISSN (Online): 2320-9364, ISSN (Print):
 2320-9356.
- 10 **S.Chandra Sekhar**, J.Lakshmi Prasanna, **“128 Bit Parallel Prefix Tree Structure
 Comparator”** International Journal of Research in Engineering and Science (IJRES), Volume 3
 Issue 10, October. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.
- 11 **Chandrasekhar Savalam**, Prasanti Korapati, **“Implementation and Design of AES S-Box on
 FPGA”** International Journal of Research in Engineering and Science (IJRES), Volume 3 Issue 1,
 Jan. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.

National/International Conferences (National and International)

1. Korapati Prasanti, Kalpana Seelam, Ch Jayalakshmi, **Chandra Sekhar Savalam** **“Preventive
 system for forests property using wireless communication”** in IEEE 2019 5th International
 Conference on Advanced Computing & Communication Systems (ICACCS), JCT Institutions
 Engineering and polytechnic, Pichanur, COIMBATORE, TAMIL NADU, INDIA
2. K Prasanti, S Kalpana, **Chandra Sekhar Savalam** **“A Novel Feature of Increased Safety
 During Car Crashes”** in IEEE 2nd International Conference on Inventive Systems and Control
 (ICISC 2018) organized during 19th and 20th January, 2018, JCT Institutions Engineering and
 polytechnic, Pichanur, COIMBATORE, TAMIL NADU, INDIA
3. K Prasanti , Ch JayaLakshmi , **S.Chandra Sekhar** **“A prototype controlling of barrage gates
 using Arduino under flood conditions”** in (IET&IEEE) 3rd International conference on
 Electrical, Electronics, Engineering Trends, Communication, Optimization and
 Sciences(EEECOS-2016) organized by SASI Institute of Technology & Engineering,
 Tadepalligudem, Westgodavari District, Andhrapradesh, India, During 1st to 2nd ,June 2016.
- 4 Ch.Jaya Lakshmi ,K.Prasanti, **S.Chandra Sekhar** **“Performance Analysis of Image Fusion for
 Different Wavelet Families”** in (IEEE) March 2016 International Conference on Innovations
 in information Embedded and Communication Systems (ICIIECS'16) organized during March
 2016, Karpagam College of Engineering, Coimbatore, COIMBATORE, TAMIL NADU, INDIA.

List of Workshops/Seminars/Guest lectures/FDPs/Webinars- Attended

- 1 Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy **Online FDP on "Lab on Chip"** from **2021-1-18** to **2021-1-22** at **S.R. M INSTITUTE OF SCIENCE AND TECHNOLOGY**.
- 2 Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy **Online FDP on "Sensors Technology"** from **2021-1-4** to **2021-1-8** at **S.R. M INSTITUTE OF SCIENCE AND TECHNOLOGY**.
- 3 Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy **Online FDP on "Micro-electromechanical Systems"** from **2020-11-23** to **2020-11-27** at **SARDAR PATEL COLLEGE OF ENGINEERING**.
- 4 Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy **Online FDP on "Internet of Things (IoT)"** from **2020-11-14** to **2020-11-18** at **Indian Institute of Technology Goa (IIT Goa)**.
- 5 Actively participated in AICTE sponsored short term training program on **"Trends and Challenges in Medical Image Analysis Through Deep Learning Algorithms"** Phase-I, Organised by Department of Electronics and Instrumentation Engineering from 26th October to 31st October, 2020.
- 6 Attended "A 3-Day Online Certification Program on **Designing FPGA's Using the Vivado Design Suite**" held from 8th June to 10th June 2020, by sandeepani school of Embedded System Design, Bangalore in association with CoreEL Technologies and Xilinx.
- 7 Attended "A One Week National Level Online Faculty Development Program on **Innovation to Academics**" organized by the Ramachandra College of Engineering, Eluru, in association with National Science & Technology Entrepreneurship Development Board (NSTEDB) Conducted from 11-05-2020 to 16-05-2020.
- 8 Attended "A 4-Day Online FDP on **Data Science and Machine Learning using Python**" organized by the Department of Electronics and Computer Engineering, K L University held on 20th-23rd April 2020.
- 9 Attended a Webinar on **"Challenges and Importance of e-Waste management"**, organized by the IEEE Communications Society Students chapter, Department of Electronics and Communication Engineering, Sreenidhi Institute of Science and Technology, Hyderabad in association with IEEE product Safety Engineering Society, India through Online.
- 10 Participated in the International Webinar on **"Nano-Structured thin films for Multifunctional Applications"** organised by the Departments of Chemistry, Physics & Electronics, on 11th June 2020.
- 11 Attended **"A Webinar on Artificial Intelligence & Machine Learning-Challenges, Opportunities and Emerging Applications"** organized by the Department of Electronics and Computer Engineering, K L University held on 30th April 2020.
- 12 Attended **"A Webinar on Machine Learning Techniques for detection and classification"** organized by the Department of Electronics and Computer Engineering, K L University held on 25th April 2020.
- 13 Attended **"A Webinar on Examination reforms to complement OBE requirements"** organized by the Department of Electronics and Computer Engineering, K L University held on 24th April 2020.
- 14 Attended the Webinar on **"Variable Frequency Drives – Cutting Edge Technology"** organized by Department of Electronics and Communication Engineering, Easwari Engineering College on 14th May, 2020.
- 15 Attended an International Webinar on **Microfabrication Technology** organized by the Dept. of Computer Science and Engineering, JIS College of Engineering, Kalyani, Nadia, West Bengal, India, on the 9th of July 2020.
- 16 Attended an International Webinar on **Time Series Analysis, Modeling and Forecasting** organized by the Dept. of Computer Science and Engineering, JIS College of Engineering, Kalyani, Nadia, West Bengal, India, on the 4th of July 2020.
- 17 Participated in the Webinar on **"Ensuring Quality Assurance In Higher Education – Role Of Industry & Professional Bodies"** organized by Internal Quality Assurance Cell (IQAC) and Department of Computer Science & Engineering under **AICTE MARGDARSHAN** on 1st June, 2020.

- 18 Attended an International Webinar on **Block Level Design Using IP Integrator in Xilinx Vivado** held on 26th of June 2020 in association with CoreEL Technologies and Xilinx.
- 19 Attended the Webinar series” **The vision and Goals with core values of IEEE, Technology Entrepreneurship Development, Role of IEEE 802 standards family during COVID-19 and post COVID-19 era,**” organised by IEEE student branch, Department of Electronics and Communication Acropolis Institute of Technology and research, Indore during 18-05-2020 to 20-05-2020.
- 20 Attended a One Week INUP hands-on training workshop on “**Nano Fabrication Technologies**” Held at **IIT Bombay**, Mumbai during October 22nd to 26th 2018.
- 21 Attended a One Day National Convention on “**Research Trends in Wireless Communication Technologies**” organized by Dept.of ECE, PVPSIT, in association with **National Instruments India** on 29th July 2017.
- 22 Attended a Five Day Workshop on “**VLSI Design**” organized by **APSSDC (Andhra Pradesh State Skill Development Corporation) Department of Skill development, Entrepreneurship & Innovation, Government of Andhra Pradesh** during 15th to 19th Nov 2016 at Dept. of ECE, SRK Institute of Technology, Enekepadu, Vijayawada.
- 23 Attended a Five day Workshop on “**Recent Advancements in VLSI Technology and Design Using EDA Tools**” organized by Dept.of ECE, UCEK(A), JNTUK, KAKINADA during 20th to 24th July 2016.
- 24 Attended a Three day Workshop on “**Custom IC Design and ASIC Implementation Using Cadence EDA Tools**” organized by Dhanekula Institute of Engineering &Technology, Ganguru on 3rd, 4th and 5th of August 2016.
- 25 Participated One Week Faculty Development Program (FDP) on “**Outcome Based Education (OBE)**” jointly organized by Electronics &ICT Academy, NIT Waranghal & Dhanekula Institute of Engineering and Technology, Vijayawada, during 06-06-2016 to 11-06-2016. And I Was awarded **A⁺** (A-Plus) Grade in the Test conducted.
- 26 Attended Two day Faculty Development Program (FDP) on “**Analog system Design using ASLK Pro**” Organized by Dhanekula Institute of Engineering and Technology, Ganguru during 16th to 17th February 2016.
- 27 Attended a Two day Workshop on “**Signal and Image processing Applications on Xilinx FPGAs**” organized by Dept.of ECE,VR Siddhartha Engineering College, Vijayawada during 26th to 27th February, 2015.
- 28 Attended a Two day Workshop on “**Advanced in VLSI Technology(NW-AVLSI 2013)**” organized by Dept.of ECE,UCEK(A),JNTUK,KAKINADA during 26th to 27th April, 2013.
- 29 Attended One day Faculty Development Program (FDP) on “**LATEX**” Organized by Dhanekula Institute of Engineering and Technology, Ganguru on 4th may, 2013.
- 30 Attended a Three day Workshop on “**DSP Processors**” organized by Dept.of ECE, mallareddy College of Engineering and Technology,Hakimpet,Secunderabad during 28th to 30th August 2008.
- 31 Attended a Three day Workshop on “**Digital Design And Verification Using Verilog**” organized by Dhanekula Institute of Engineering&Technology, Ganguru during 12th to 14th February 2013.
- 32 Attended a One day Faculty Development Program (FDP) on “**JNTUK Centre of Excellence for e-resources Development & Deployment(CoEeRD)**” organized by JNTUK CoEeRD and Globarena technologies Pvt.Ltd on 28th August 2014.
- 33 Attended Three day Faculty Development Program (FDP) webinar on “ **Networking Simulation using NetSim Software**” Organized by DELLSOFT Technologies Pvt.Ltd during 15th to 17th June, 2016.
- 34 Attended Three One Faculty Development Program (FDP) webinar on “**Circuit Simulation and PCB Design**” Organized by DELLSOFT Technologies Pvt.Ltd during 14th June, 2016.

List of Certification Courses Done

NPTEL/Swayam

- 1 NPTEL Online Certification: **Digital Circuits** (77%), Got ELITE Certification, Jul-Oct 2018, IIT Kharagpur. (12-week Course. No of Credits recommended by NPTEL:3)


2 NPTEL Online Certification: **Switching Circuits and Logic Design** (84%), Got ELITE Certification, Jul-Oct 2019, IIT Kharagpur. (12-week Course. No of Credits recommended by NPTEL:3)

Coursera Certifications

	Name of the Course	Percentage
3	VLSI CAD-I: Logic	85
4	VLSI CAD-II: Layout	83
5	Developing FPGA accelerated cloud applications with SDAccel: Theory	98
6	Developing FPGA accelerated cloud applications with SDAccel: Practice	100
7	FPGA Computing systems: Background knowledge and introductory material	99
8	Introduction to OpenCL on FPGAs	100
9	Sensors and Sensors Circuit Design	91
10	Wireless communication for everybody	100
11	Introduction to programming with MATLAB	99
12	Introduction to communication science	100
13	Electric power systems	98
14	AI for everyone	100
15	Programming for everybody (getting started with Python)	98
16	Introduction to HTML	100

Achievements / Awards etc.,

1. UGC NET Qualified in ELECTRONIC SCIENCE: December 2014
2. UGC NET Qualified in ELECTRONIC SCIENCE: June 2014
3. GATE Qualified: 2012
4. GATE Qualified: 2009



(Chandra Sekhar Savalam)