
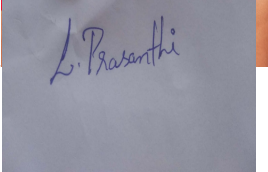


|  |  |            |   |   |
|--|--|------------|---|---|
| Full Name (BLOCK LETTERS)              | LINGALA PRASANTHI  |            | Passport size Photograph  |   |
| E-Mail Id                              | prasanthi024@yahoo.com   |            |  |   |
| Phone No.                              | 9381201329   |            |   |   |
| Residential Address                    | D.no: 7-11, Islam nagar ,near ramalayam,<br>Challapali-521126  |            |   |   |
| Father's Name                          | Syamala Rao  |            |   |   |
| Mother's Name                          | Kusuma   |            |  |   |
| Date of Birth                          | 25-08-1993   |            |   |   |
| Caste & Sub Caste                      | BC-A& Rajaka   |            |   |   |
| Date of Joining this Institute         | 22-06-2019   |            |   |   |
| Designation & Department               | Lecturer&DECE  |            |   |   |
| Areas of Specialization                | VLSI & EMBEDDED SYSTEMS  |            |   |   |
| <b>Academic Data</b>                   |  |            |   |   |
| Qualification                          | Year of Pass   | Percentage | Name of the University  | Name of the College                             |
| MTECH                                  | 2017   | 70         | JNTUK   | Usha Rama College of Engineering and Technology |
| BTECH                                  | 2014   | 77         | JNTUK   | V.R Siddhartha College of Engineering           |
| Diploma(ECE)                           | 2011   | 85         | SBTET   | Govt. polytechic Vijayawada                     |
| <b>Work Experience in years</b>        |  |            |   |   |
| Teaching                               | Industry   | Research   | Others  | Total   |
| 3                                      |  |            |   | 3   |
| <b>Subjects handled (last 5 years)</b> |  |            |   |   |
| DIPLOMA Level                          | Electronic circuits, Linear integrated circuits and applications, Computer Hardware, Microprocessors, Electrical Technology. |            |   |   |
| PG Level                               | -  |            |   |   |

| <b>Research &amp; Publications</b> |                         |              |
|------------------------------------|-------------------------|--------------|
| No. of Papers Published in         |                         |              |
| National Journals :                | International Journals: | Conferences: |
| Details of Patents                 |                         |              |
| Details of Books Published         |                         |              |
| Details of Awards Received         |                         |              |
| No. Of Projects Guided             | UG Level :              | PG Level :   |
| No. Of Ph.D Guided / Guiding       |                         |              |